

Resume



DR. PRIYADARSHINY DHAR
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My professional objectives are always to put all my abilities and acquired skills to the best possible use in my career. I am always keen to enrich my knowledge and technical skills through continuous learning of new technologies and concepts. I look forward to grow in my career and enrich professional experience through dynamic & challenging job position.

Profile Summary

Total years of Experience : 18+ years
Current Designation : Assistant Professor

Formal Educational Qualification

Discipline : Electronics/Electrical Engineering
Ph.D : Signal processing technique, Image processing & Machine Learning
Post-Graduation : M.E (Control Systems)
Graduation : B.E (Electronics)

Qualification	Institution / Board	Year of Passing	Percentage Obtained
Ph.D	IIT(ISM),Dhanbad	Awarded (May,2022)	-
M.E (Master in Control Engineering.)	Jadavpur University, West Bengal, India	2007	8.13(CGPA)
B.E (Electronics)	Kavikulguru Institute of Technology & science, Maharashtra, India	2002	65%

Professional Experience Details

May 2009 to till date: Netaji Shubhas Engg College, West Bengal

Designation	Assistant Professor
Institution / Location	Netaji Shubhas Engg College, Garia, West Bengal
Duration	May 2009 to till Date
Subjects overseas	<p>Theory</p> <ul style="list-style-type: none">• Control Theory• Field Theory• Power Electronics• Microprocessor• Analog Integrated Circuit• Circuit Theory• Basic Electronics• Discrete Electronics• Electronics Device & Circuit• Electronics System Device• P-Spice• Industrial Electronics• Communication Theory• Digital Electronics• Digital Control• AI <p>Practical</p> <ul style="list-style-type: none">• Control system• Power Electronics• Telemetry & Remote Control• Electrical Measurement• Circuit Theory• Process Control• Communication• AI using Prolog
Responsibilities	<ul style="list-style-type: none">• Subjects taken for all the years of B.E• Lab conducted for all the years of B.E• Practical Examiner in University of Technology□• Theory Examiner in University of Technology• Final Year Projects• NSS

September 2002 to May 2005: JIS Engineering College, West Bengal

Designation	Assistant Professor
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Institution / Location	JIS Engineering College, Kalyani, West Bengal.
Duration	September 2002 to May 2005
Subjects overseas	<p>Theory</p> <ul style="list-style-type: none"> • Basic Electronics • Electronics Device & Circuit • Electronics System Device • Communication Theory • Power Electronics • Digital Electronics • Basic Electronics • Circuit Theory • Artificial Intelligence • Analog Electronics • Microprocessor & Microcontroller • Control System • <p>Practical</p> <ul style="list-style-type: none"> • Circuit Theory • Switching Theory • Communication • Electrical Measurement • Basic Electronics • Control system • Power Electronics • Telemetry & Remote Control • Microprocessor • Digital Electronics • Artificial Intelligence
Responsibilities	<ul style="list-style-type: none"> • Subjects taken for all the years of B.E • Lab conducted for all the years of B.E • Practical Examiner in University of Technology • Theory Examiner in University of Technology • Final Year Projects

Project / Thesis Details

Title	Boi-electrical Signal Classification (Ph.D)
Institute	Indian Institute of Technology (ISM), Dhanbad.
Guide	Prof. V. Mukherjee & Prof. S. Dutta

Publication	i. 1 International journal (Impact Factor 5.1) ii. 3 International Journal (ESCI) iii. 3 International Conference (One IEEE) <i>(Refer additional sheet for details)</i>
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Title	BIO-ELECTRICAL SIGNALS CLASSIFICATION USING CROSS-WAVELET & MACHINE LEARNING
Period (PhD)	From April 2016 to May 2022
Institute	IIT(ISM) Dhanbad

Title	Observer design for a linear state -delay system using generalized matrix inverse.
Period (M.E)	M.E Final year – From June 2006 to May 2007
Institute	Jadavpur University, W.B, India
Guide	Prof. Gourhari Das
The Project	<p>Design reduced order state observers for linear state delay system for lateral missile autopilot for a class of missiles employing state space technique. It is intended to use reduced order observer system designed by the generalized matrix inverse method.</p> <p>A MATLAB program has been developed for comparing the response of the two loop autopilot with and without state delay, observer parameters, determining observer gain, comparing the response of the observer of the system with and without state delay</p>
Special Software/ Tools used	MATLAB , P-Spice, Lab-View, MP-IDE-Lab, Python, Prolog

Title	Electronic answering and recording Telephone machine with caller id system
Period (B.E)	B.E Final year
Institute	Kavikulguru Institute of Technology & science, India
The Project	Design and developed a prototype of Electronic answering and recording Telephone machine with caller id system by which the called person can store the information (caller identification and voice message) of calling person in case of unavailable.

Title	Vocational Industrial Training
Period (Training)	B.E - 3 rd year
Institute	Nicco Corporation LTD, West Bengal, India
	Nicco is a local cable, switchgear and other small electrical parts manufacturing company in West Bengal, India. The training was meant to understand the production process and working methodologies of the company.

Training/Short Term Course

Sl.No.	Title	Organization Name	Duration	Venue
1.	Short Term Course on "Power Electronics"	Under the aegis of NaMPET	6 Days(2009)	Department of Electrical Engineering, Bengal Engineering & Science University, Shibpur
2.	"Lab View and PIC Microcontroller Application"	Power and Energy Chapter, IEEE Calcutta Section	June 28 –July 03,2010	Department of Electrical Engineering, Jadavpur University
3.	Faculty Development Program	Enterprise Development Institute	2 nd Jan-13 th Jan,2012	IB-194,Sec-III, Salt lake, Kol-106
4.	Two Week ISTE Workshop on Analog Electronics	Indian Institute of Technology Kharagpur	4 th -14 th June,2013	Netaji Subhash Engineering College, Garia
5.	Two Week ISTE Workshop on Signal & System	Indian Institute of Technology Kharagpur	2 nd -12 th January, 2014	Netaji Subhash Engineering College, Garia
6.	Two Week ISTE Workshop on Control System	Indian Institute of Technology Kharagpur	2 nd – 12 th December, 2014	Netaji Subhash Engineering College, Garia
7.	FDP on soft computing	Heritage Institute of Technology, Kolkata	7 TH -12 TH July,2016	HIT , Kolkata
8.	FDP on "Behavioral Remodeling and Use of ICT Tools for classroom Delivery of Teachers"	IIT Guwahati	9 th -15 th January,2019	NSEC, Garia
9.	Inculcating Universal Human Values in Technical Education	All India Council for Technical Education (AICTE)	18-22 nd December,2023	NSEC, Garia
10.	Advanced Machine Learning & Its Applications	NSEC	10-18 th January, 2024	NSEC, Garia
11.	Industry 6.0: Navigating the Ethical Frontier of Artificial Intelligence and Data Science	Hooghly Engineering & Technology College	25-29 June, 2024	NSEC, Garia

Computer Skills

Operating System	MS-DOS, WINDOWS 95/98/2000/XP
Language	C programming, Basic Fortran, HTML
Tool/Software	MATLAB 18, MS-Office (Word, Excel, PowerPoint etc.)

Database	MS Access 2003, SQL
Web Skills	Internet Explorer, Netscape Navigator

Personal Details

Name	PRIYADARSHINY DHAR (DAS)
Address	GD-40, Rajdanga Main Road, Kolkata- 107, India
Contact No	+919051747698(M) / +913324413052 (H)
Date of Birth	30 th September 1978
Gender	Female
Ethnicity	Asian
Nationality	Indian
Marital status	Married
Number of Children	2
Languages Speak	English, Hindi and Bengali
Interested in	Recitation, Sports (Cricket), Music and Friends

Passport Details

Passport No.	G4623209
Date of Issue	14 th November 2007
Place of Issue	Kolkata
Date of Expiry	13 th November 2017

Date: 22.08.24

Priyadarshiny Dhar

Signature

(Priyadarshiny Dhar)